AMENDMENTS TO THE CLAIMS

The listing of claims presented below will replace all prior versions and listings of claims in the application.

Listing of claims:

1. **(currently amended)** An XML processor comprising:

receiving an XML document:

a first memory storing software for performing an XML processing, variables, and values required to execute software on the received XML document;

a hardware processing module performing a part of the XML processing in a hardware manner on the received XML document, and

wherein the hardware processing module is separate and independent of the first memory storing the software for performing the XML processing;

a second memory employed by the hardware processing module; and

a CPU controlling the XML processing on the received XML document by the software stored in the first memory to generate a first output if the XML is executed by software, and to generate a second output if the part of the XML processing is performed in the hardware manner,

wherein the software is able to completely process the received XML document without the hardware processing module to generate the first output,

wherein if the hardware processing module performs the XML processing, the hardware processing is configured to perform part of the software processing without the software processing having to perform the part performed by the hardware processing.

wherein the second memory comprises:

a node memory storing the whole information that each node has to store, at least one of a node name, a node type, a parent node, a child node;

a node table managing the information stored in the node memory;

a node usage check table indicating whether to use the node table,

wherein the hardware processing module assigns storage to each node, re-assigns the storage in each node and returns the storage in each node, using the node usage check table.

2 - 3. (canceled).

4. (**previously presented**) The XML processor according to claim 1, wherein the node table has addresses in which every component on the node memory is respectively stored.

5. (original) The XML processor according to claim 1, wherein the hardware processing module performs an XML DTD processing.

6. (original) The XML processor according to claim 1, wherein the hardware processing module performs a state machine of an XML schema.

7 – 9. (canceled).

10. (**previously presented**) The XML processor according to claim 1, wherein the node table and the node usage check table have a fixed correlation.

11. (**previously presented**) The XML processor according to claim 1, wherein the node usage check table is divided into several blocks, each block indicating whether to use a corresponding node table.

12. (new) An XML processor comprising:

receiving an XML document:

a first memory storing software for performing an XML processing, variables, and values required to execute software on the received XML document;

a hardware processing module performing a part of the XML processing in a hardware manner on the received XML document, and

wherein the hardware processing module is separate and independent of the first memory storing the software for performing the XML processing;

a second memory employed by the hardware processing module; and

a CPU controlling the XML processing on the received XML document by the software stored in the first memory to generate a first output if the XML is executed by software, and to generate a second output if the part of the XML processing is performed in the hardware manner,

wherein the second memory comprises:

a node memory storing the whole information that each node has to store, at

least one of a node name, a node type, a parent node, a child node;

a node table managing the information stored in the node memory;

a node usage check table indicating whether to use the node table,

wherein the hardware processing module assigns storage to each node, re-assigns the storage in each node and returns the storage in each node, using the node usage check table, and

wherein the hardware processing module is configured to perform part of the software processing of the received XML document without the software processing having to perform the part performed by the hardware processing module.